



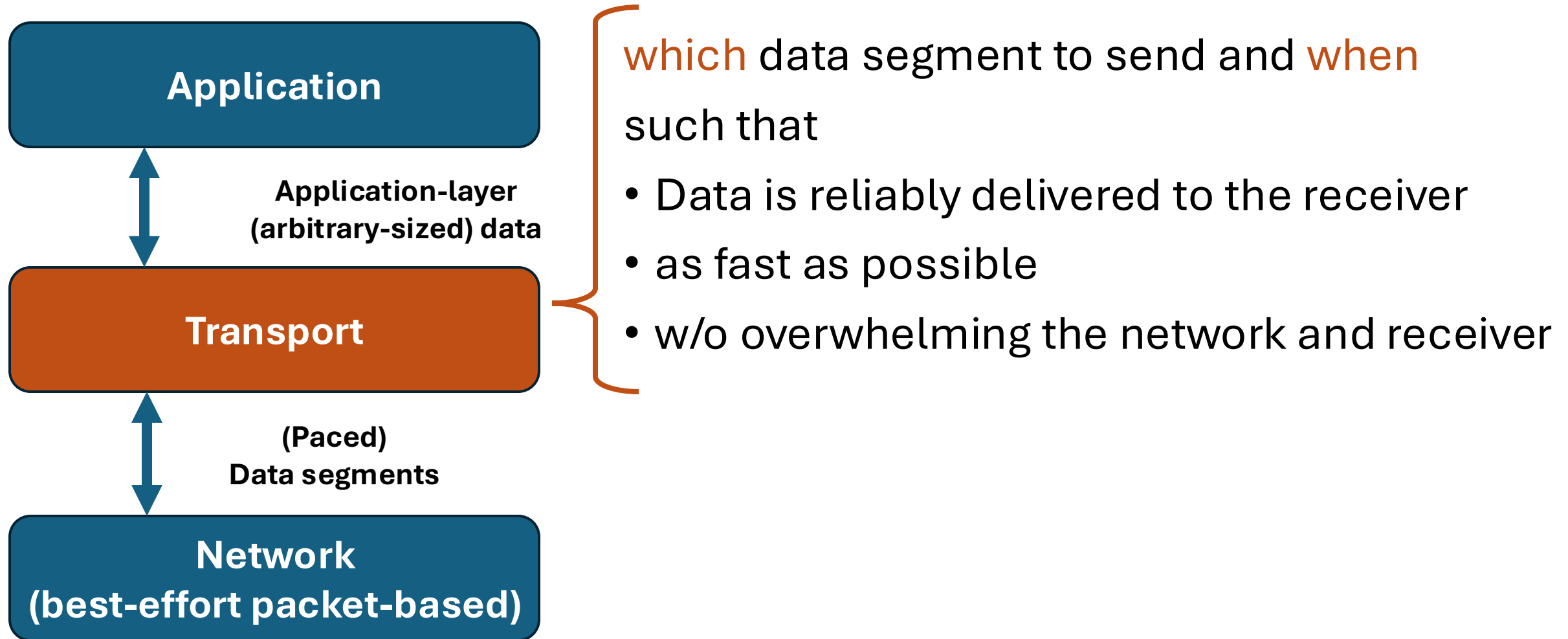
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High-Level and Target-Agnostic Transport Programs

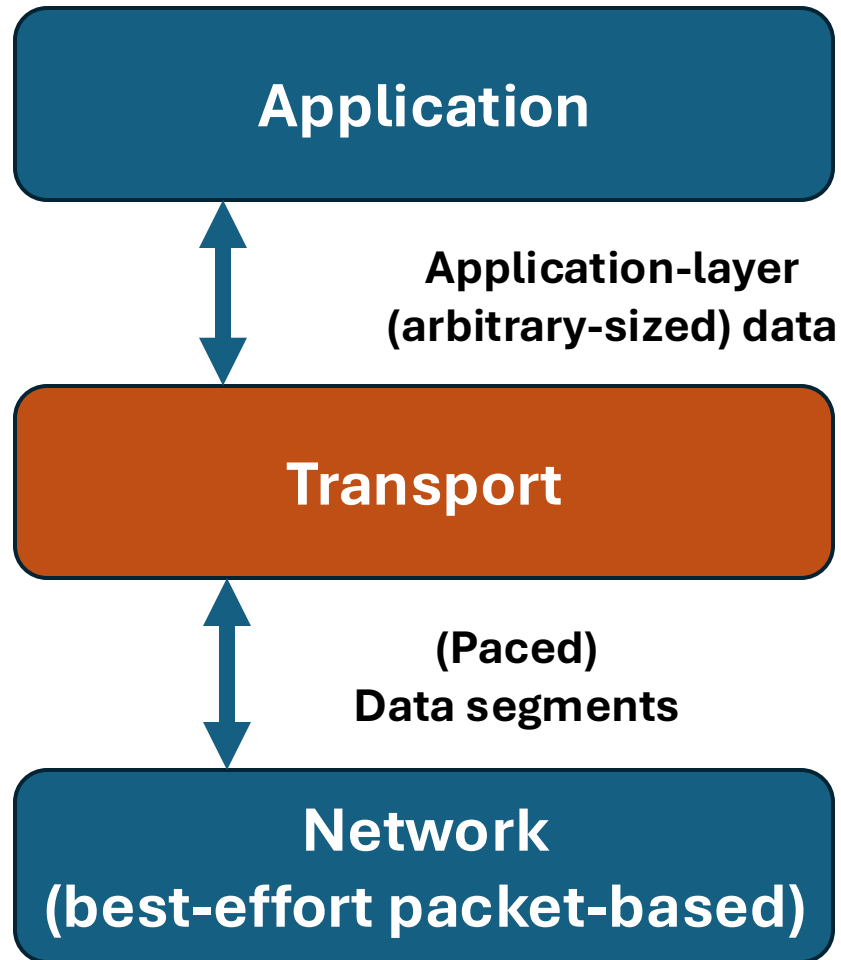
Mina Tahmasbi Arashloo
University of Waterloo

P4 Workshop 2025

No “one-size-fits-all” transport protocol



No “one-size-fits-all” transport protocol



which data segment to send and when

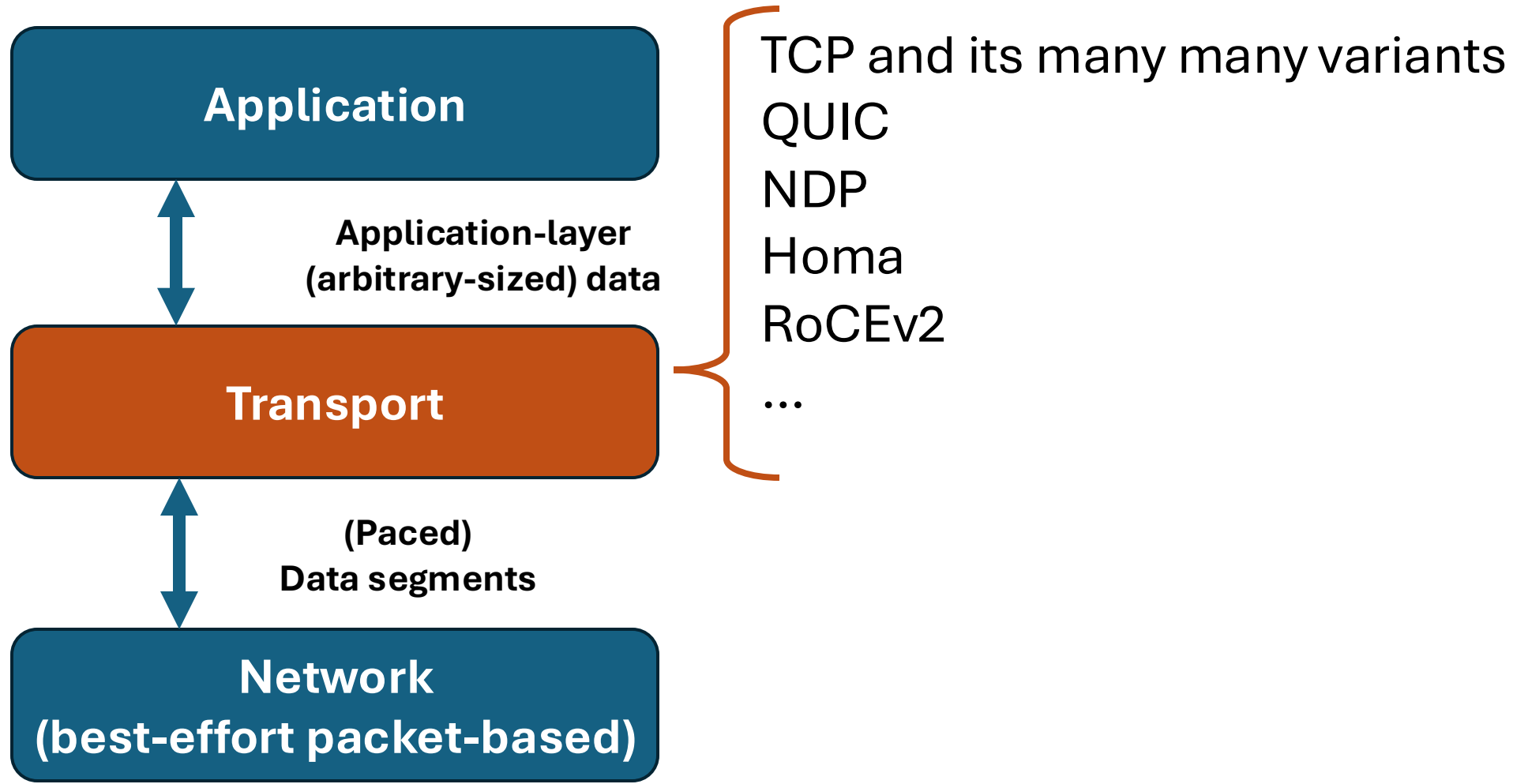
such that

• Data is reliably delivered to the receiver

Depends on

- Network characteristics
 - Wide area? Data center?
- Applications
 - Traffic patterns: small flows? Bursty?
 - Requirements: low latency? High throughput?

No “one-size-fits-all” transport protocol



The transport protocol development cycle today

No high-level specification with well-defined semantics

- Natural language documents → ambiguity
- Existing implementations → low-level target-specific code



**Pick the “right”
protocol/features**

Have to grapple with **low-level
protocol-independent issues**

- I/O, memory management,
optimized data structures, ...



Optimize

**Implement on
your “target”**



**Ensure it works
as intended**



No high-level specification with well-defined semantics

- Intended behavior is not always clear
- Pick and choose scenarios to test
- No automated high-coverage analysis and testing

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We need a *high-level*
target-agnostic
protocol-independent
programming interface for transport

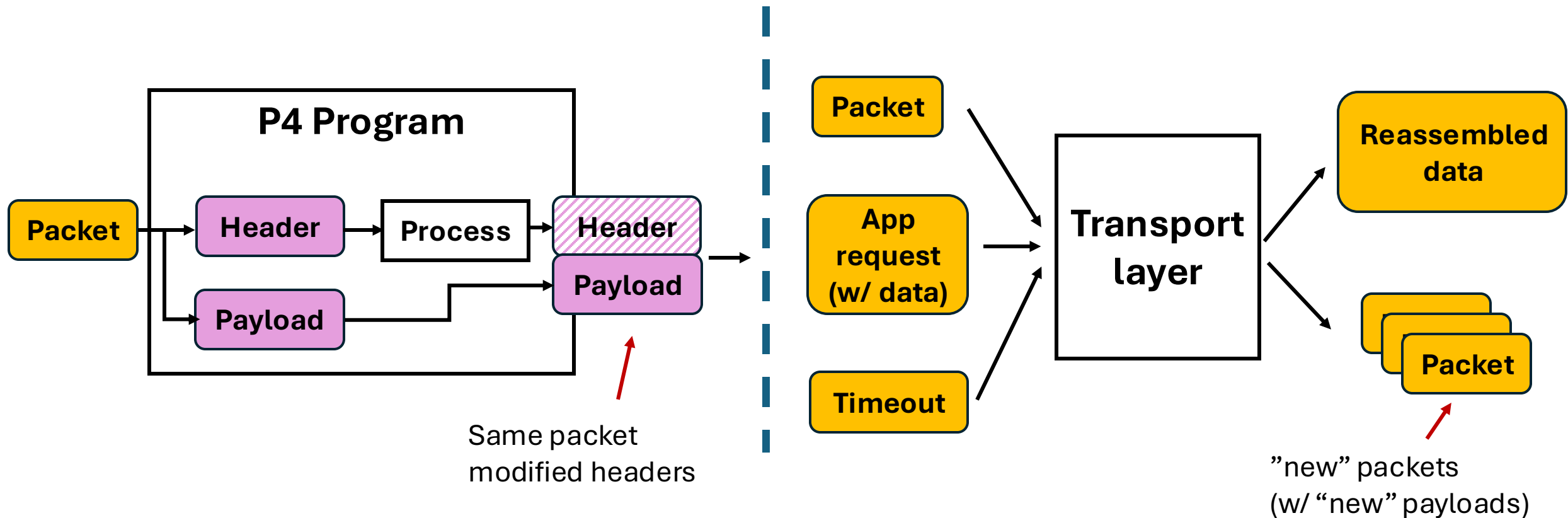
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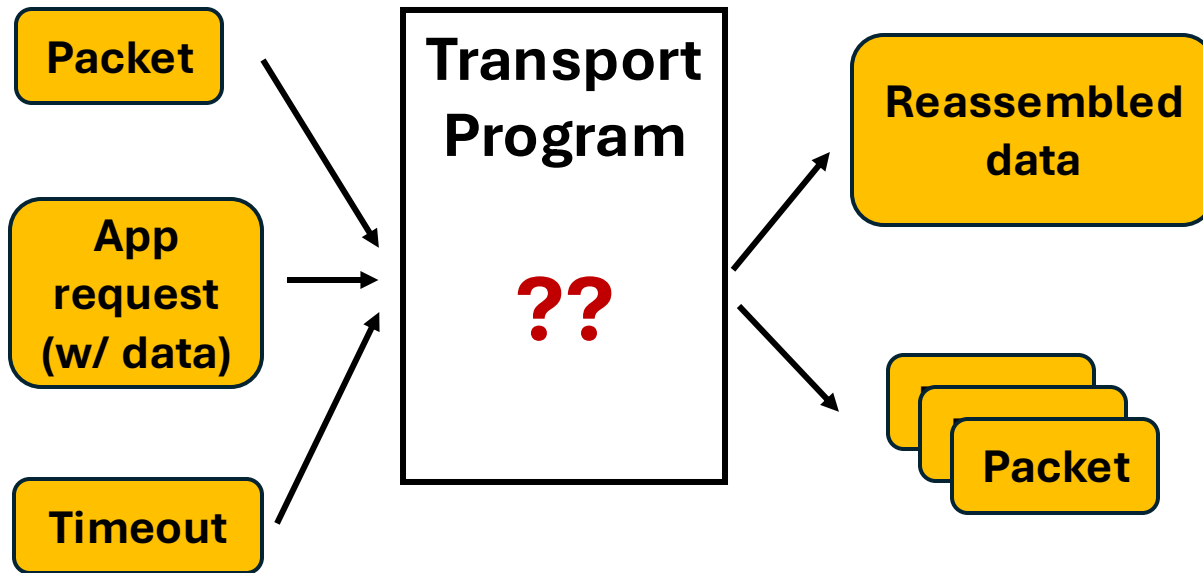
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Can't we use P4?

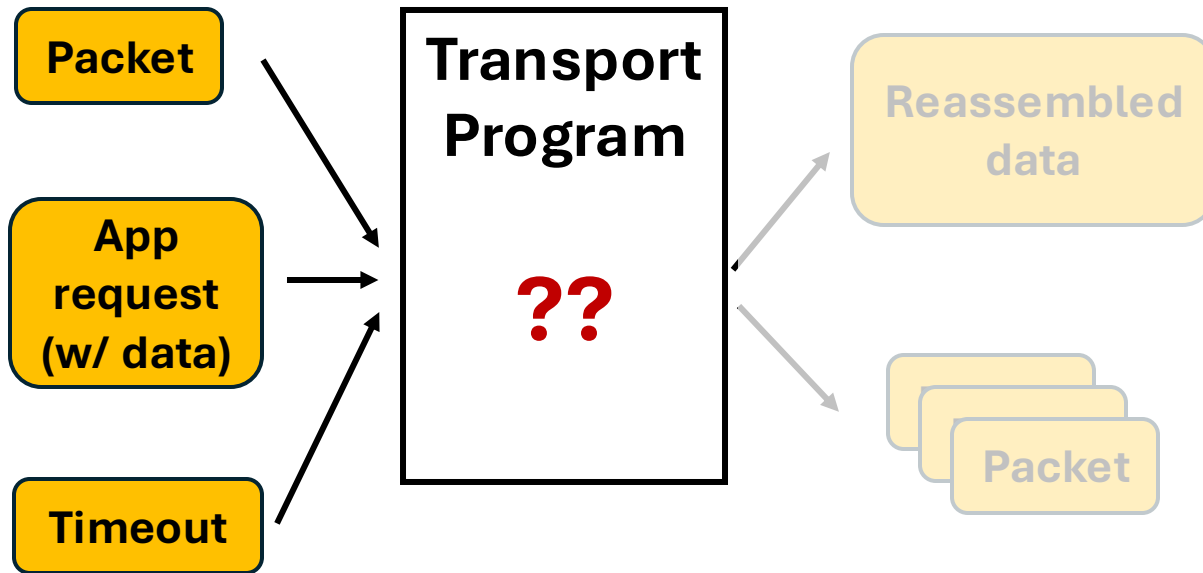
- P4 is the most widely-used high-level network programming language
- ... but for L2/L3 network functionality (i.e., routing and forwarding)



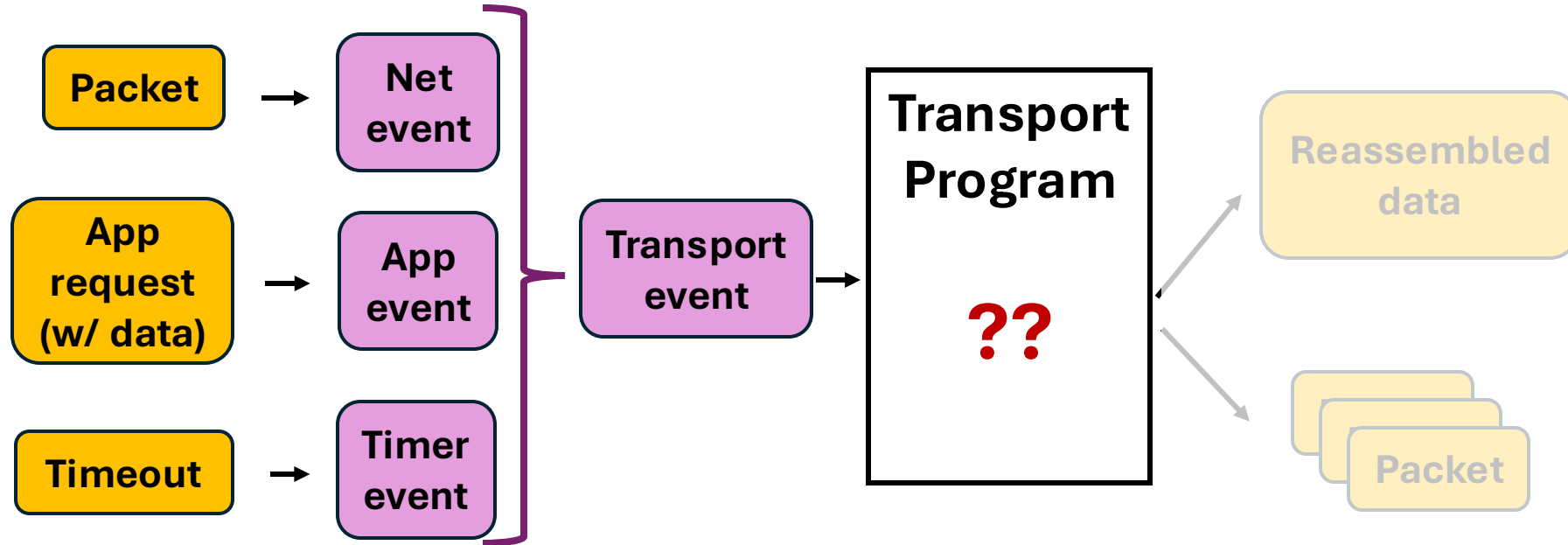
What should a transport program look like?



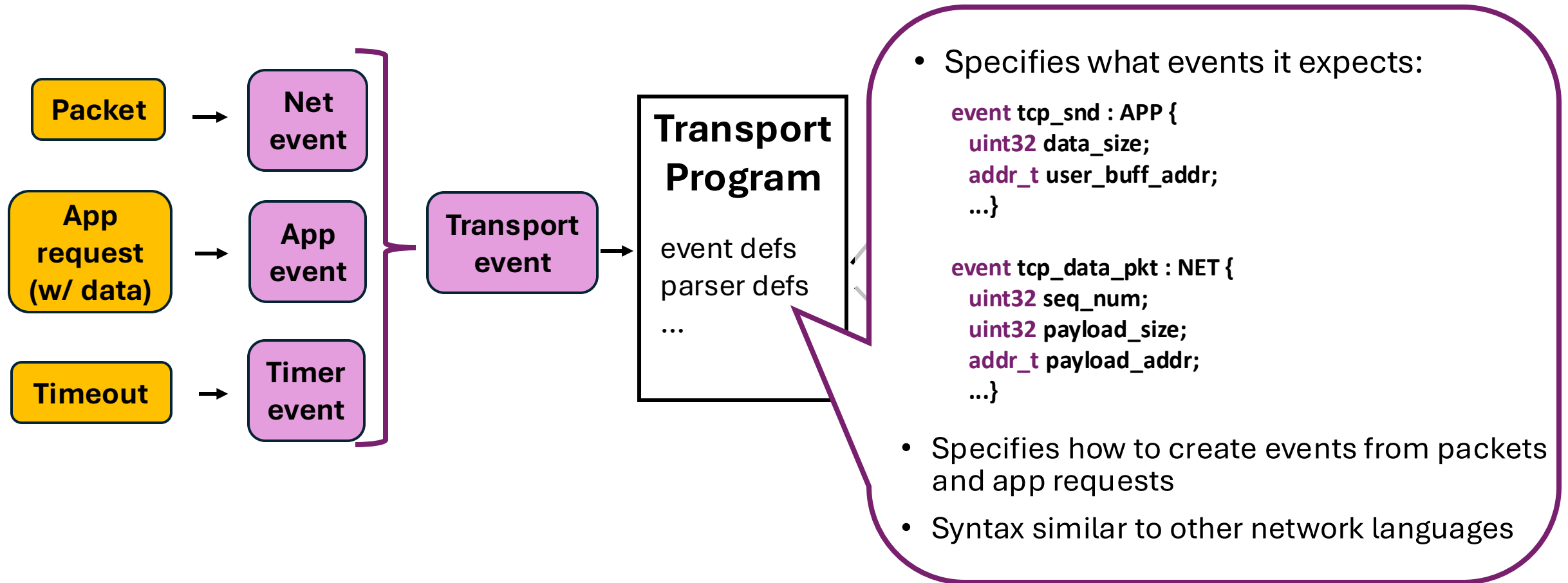
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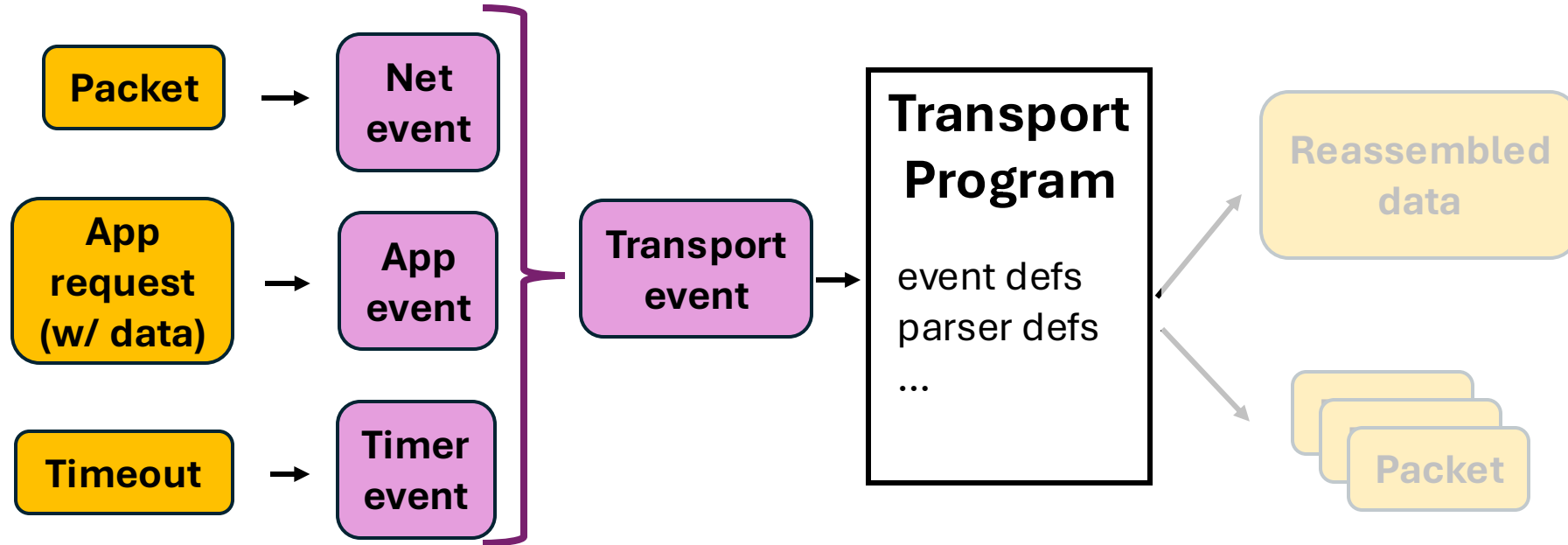
Transport events



Transport events



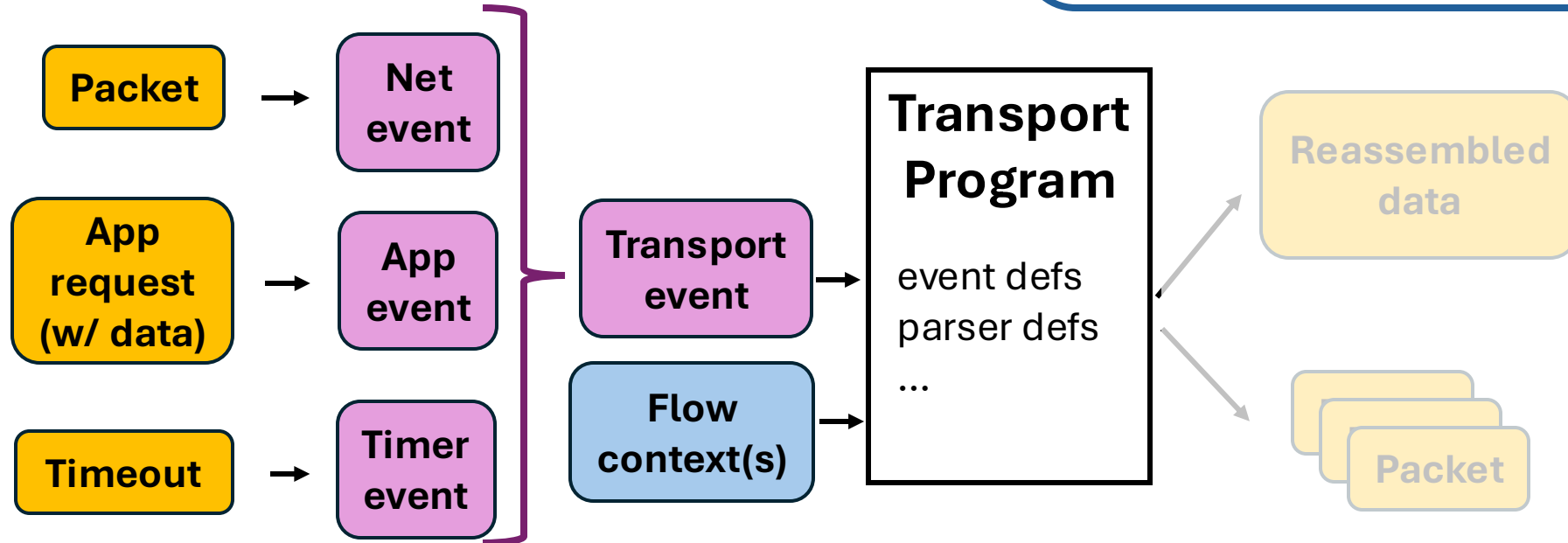
Transport events



Flow contexts

Each flow has some **state (or context)** that is

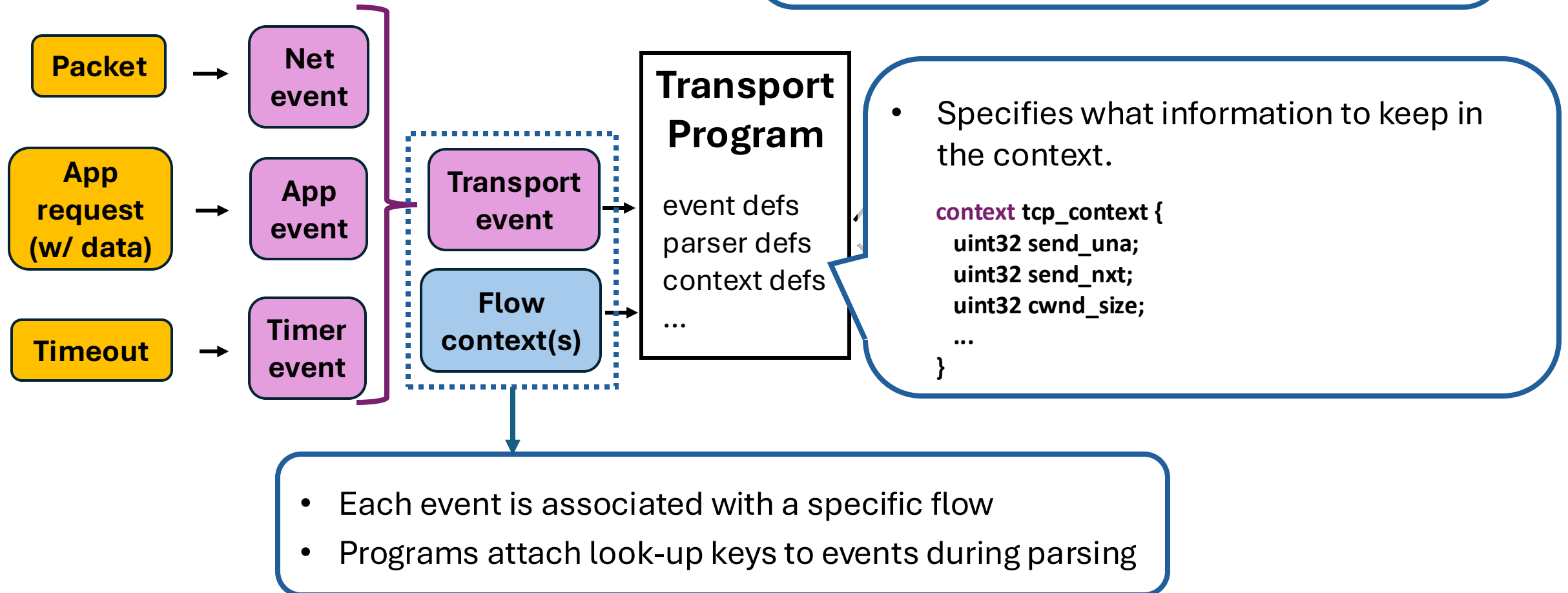
- used in event processing
- **maintained across events**
- E.g., sliding window start and end in TCP



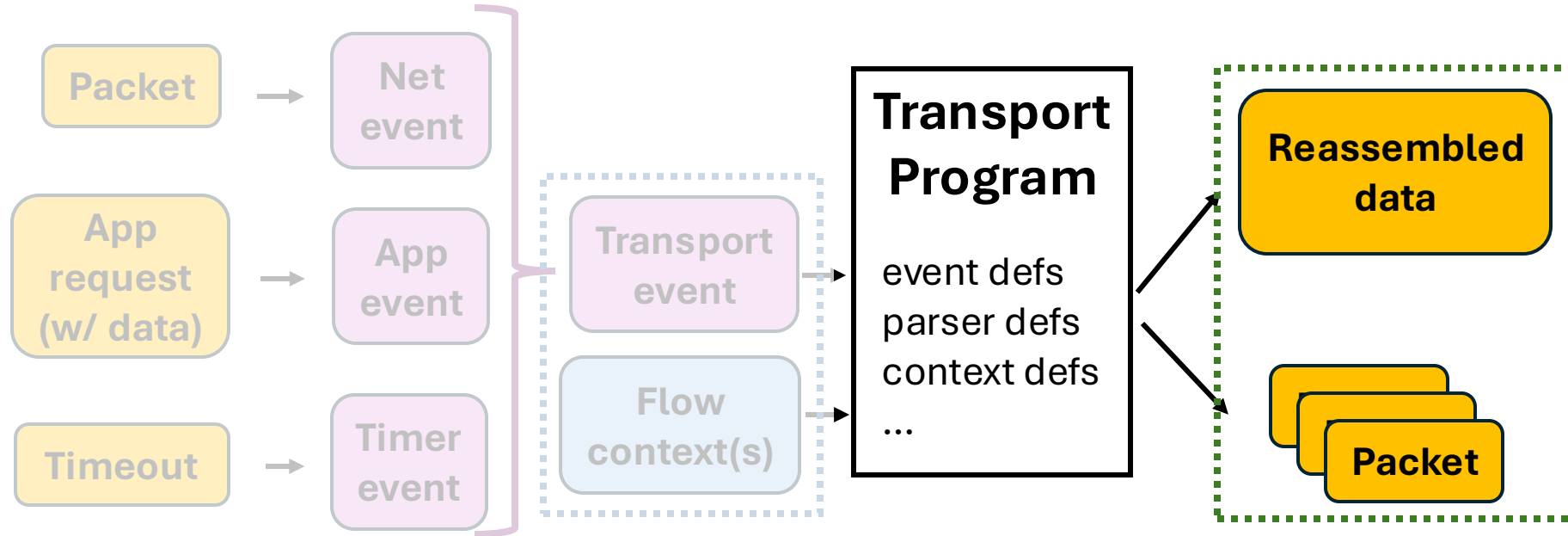
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Output: ??



Output: ??

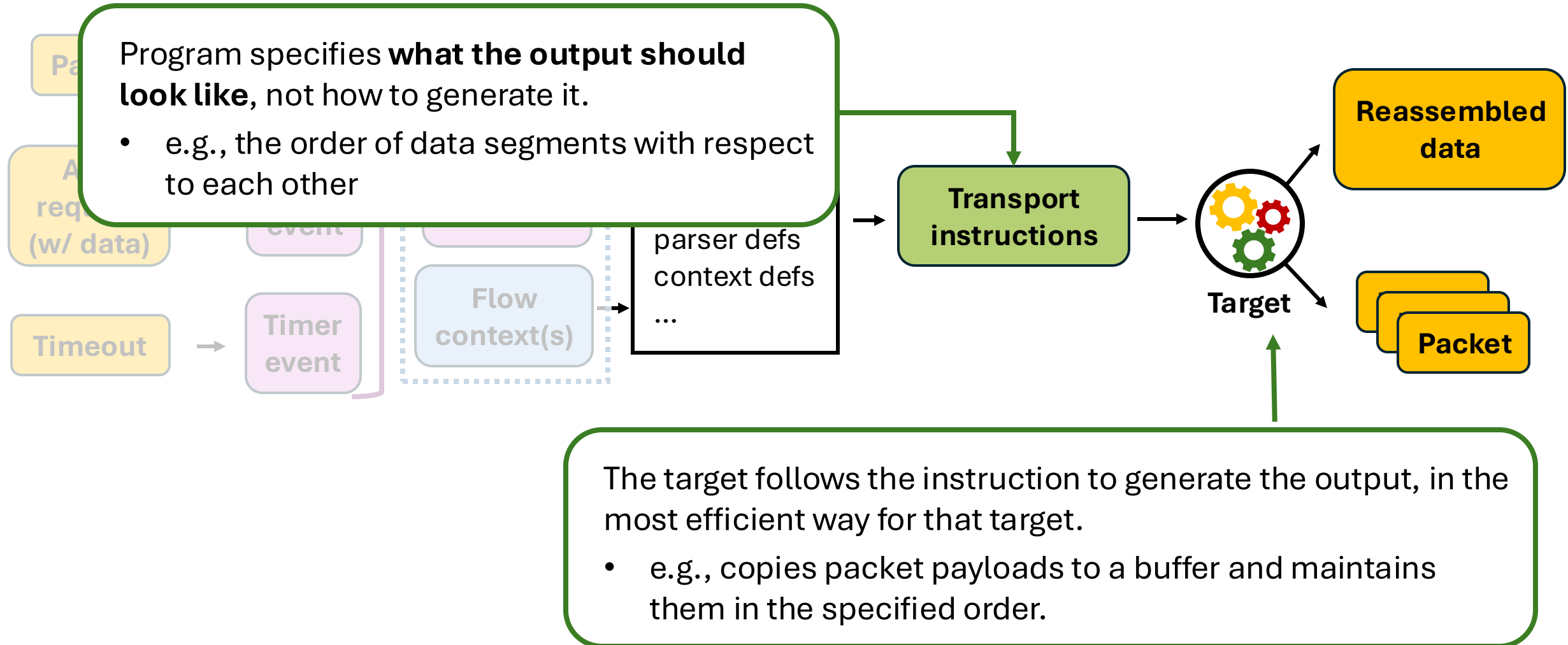
How do we **decouple**
protocol logic for reassembly and packet generation
from target-specific **implementation details**?

- Involves performance-sensitive operations:
 - Data movement
 - Buffer management
 - Packet pacing
 - ...
- The most “optimal” implementation is **target-dependent**

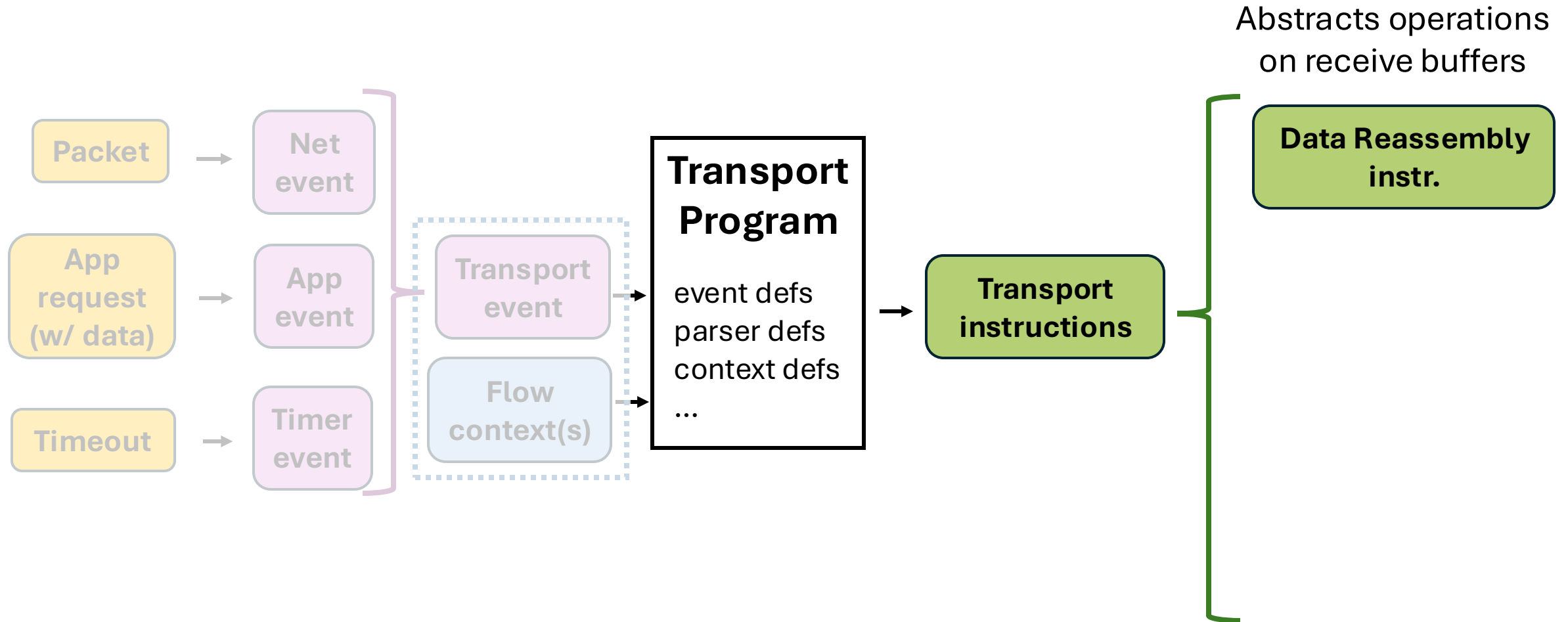
Reassembled
data

Packet

Transport instructions



Transport instructions



Transport instructions – Data Reassembly

*Transport instructions
issued by the program*

`new_rx_ordered_data(uid, size[, addr])`

- I expect to receive *size* bytes of consecutive data
 - *size* can be *INF* for byte streams
- The identifier for this “unit” is *uid*
- The data should eventually be available at *addr*

*What the target
should do*

- Allocate memory accordingly
 - Dynamic allocation?
 - Pool of buffers?
 - Zero copy (*addr*)?
 - ...
- Maintain a mapping between *uid* and the allocated space

Transport instructions – Data Reassembly

*Transport instructions
issued by the program*

`add_rx_data_seg(addr, len, uid, offset)`

- I want *len* bytes starting from *addr* to be at index *offset* of the consecutive data unit *uid*
 - *addr* → where incoming packet's payload is stored

*What the target
should do*

- Find the right “destination” memory locations based on *offset* and *uid*
- Copy data from *addr*

Transport instructions – Data Reassembly

*Transport instructions
issued by the program*

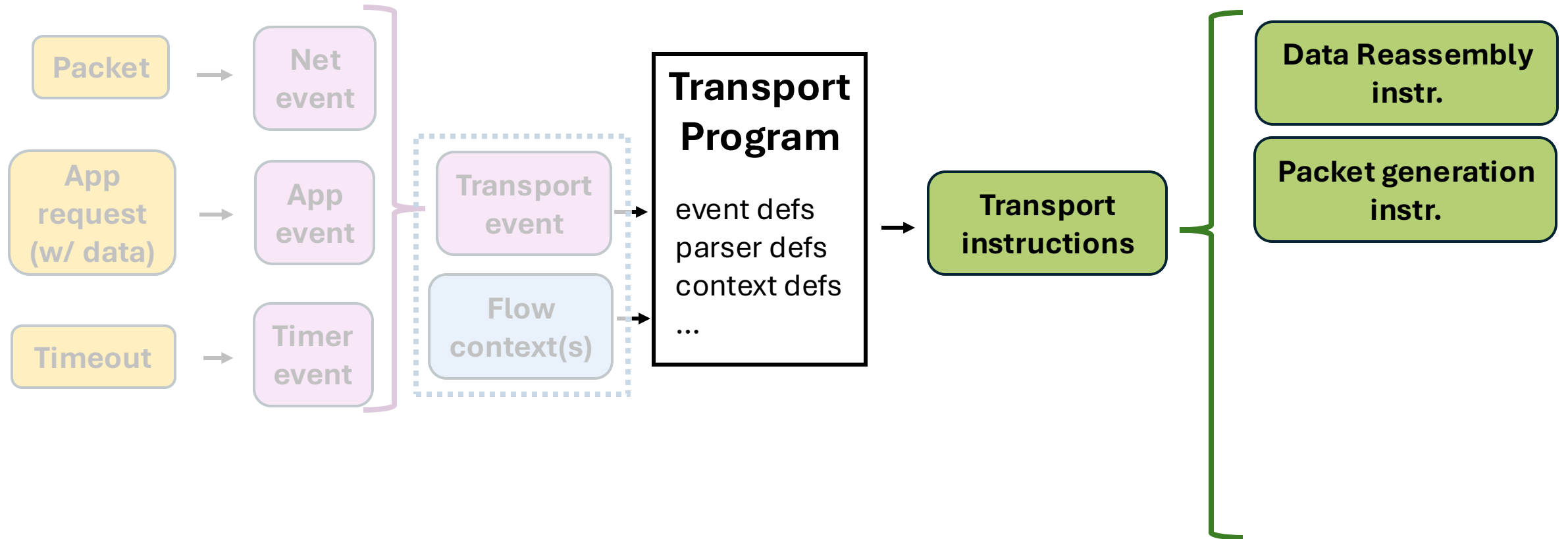
`rx_flush_and_notify(uid, len, addr)`

- I want *len* more bytes from *uid* to be made available to the application at *addr*
 - *addr* → user's buffer address

*What the target
should do*

- Keep track of how far into *uid* has been “flushed” to the app
- Find the right “source” memory locations accordingly
- Move data to *addr*

Transport instructions



Transport instructions – Packet Generation

*Transport instructions
issued by the program*

`new_tx_ordered_data(uid, size[, addr])`

`add_tx_data_seg(addr, len, uid)`

`tx_flush_and_notify(uid, len)`

- Similar to the “rx” counter-parts
- Abstracts operations on send buffers

*What the target
should do*

- Allocate memory for *uid*
- Append app data to *uid*
- Remove data from *uid*
- ...

Transport instructions – Packet Generation

*Transport instructions
issued by the program*

`pkt_gen(pkt_bp[, seg_rule_id, ...])`

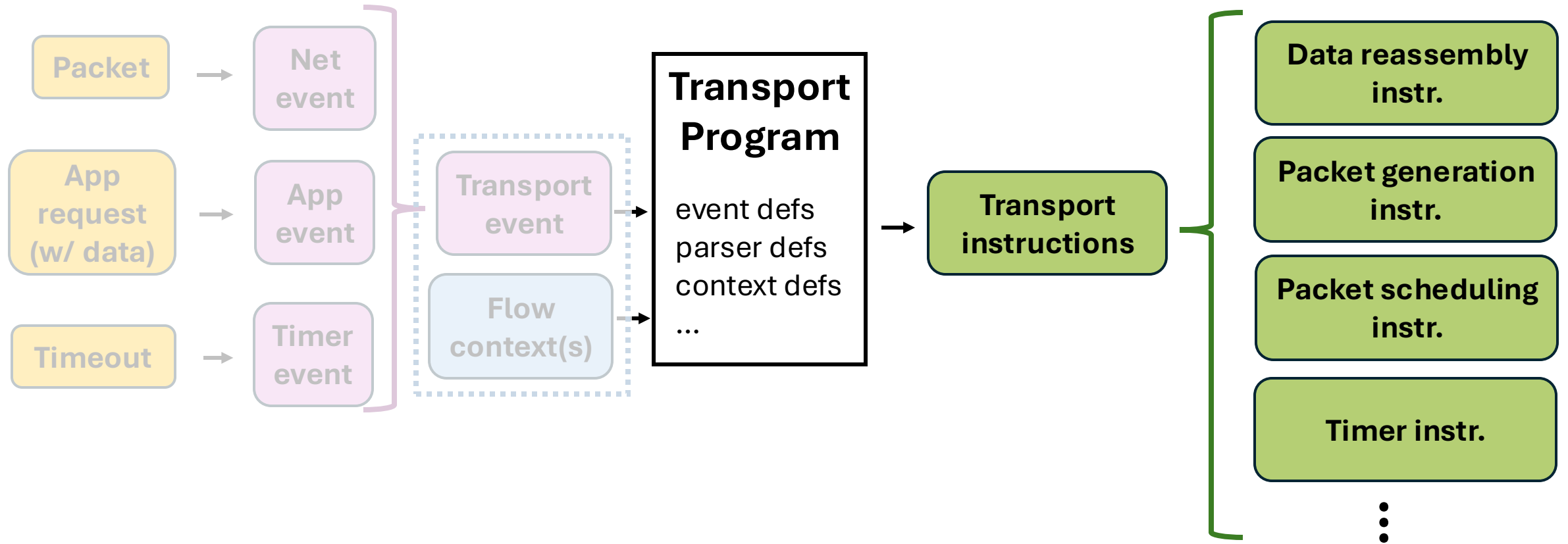
- I want packets looking like this *blueprint*
- blueprint:
 - header
 - data address and size for payload
- If data does not fit in one packet, segment it:
 - Update headers based on *seg_rule_id*

*What the target
should do*

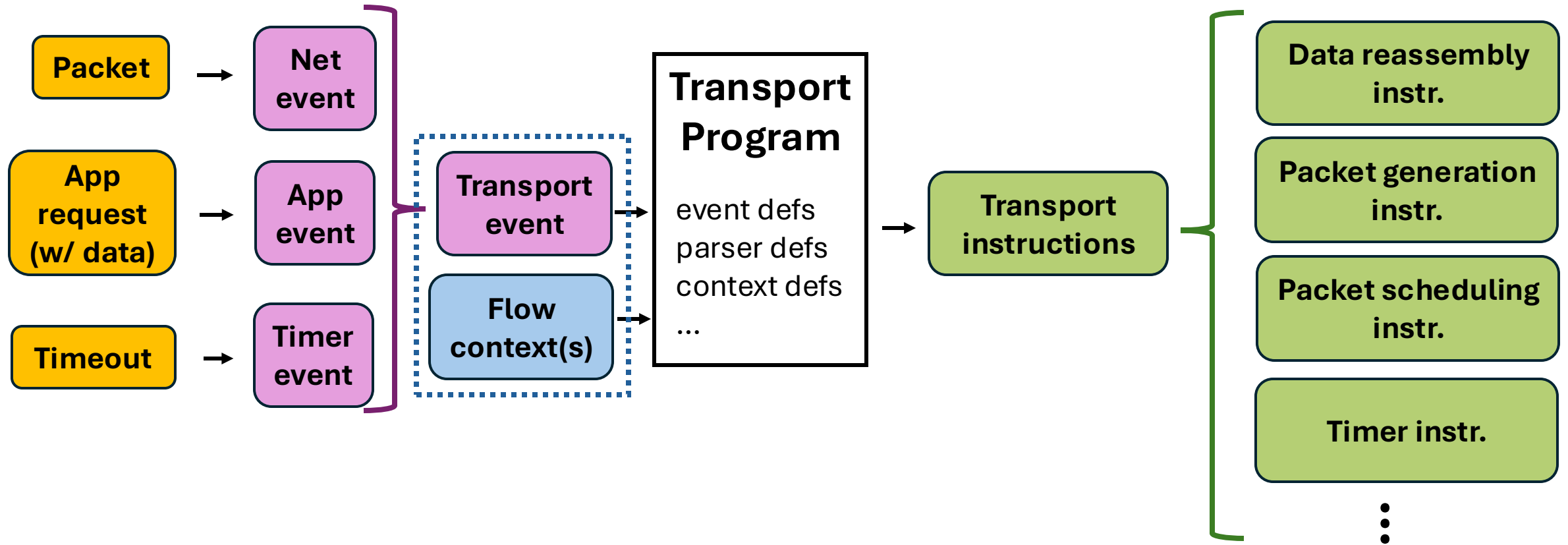
Generate the actual packets:

- Allocate packet memory
- Fill out headers
- Move data for payload
- ...

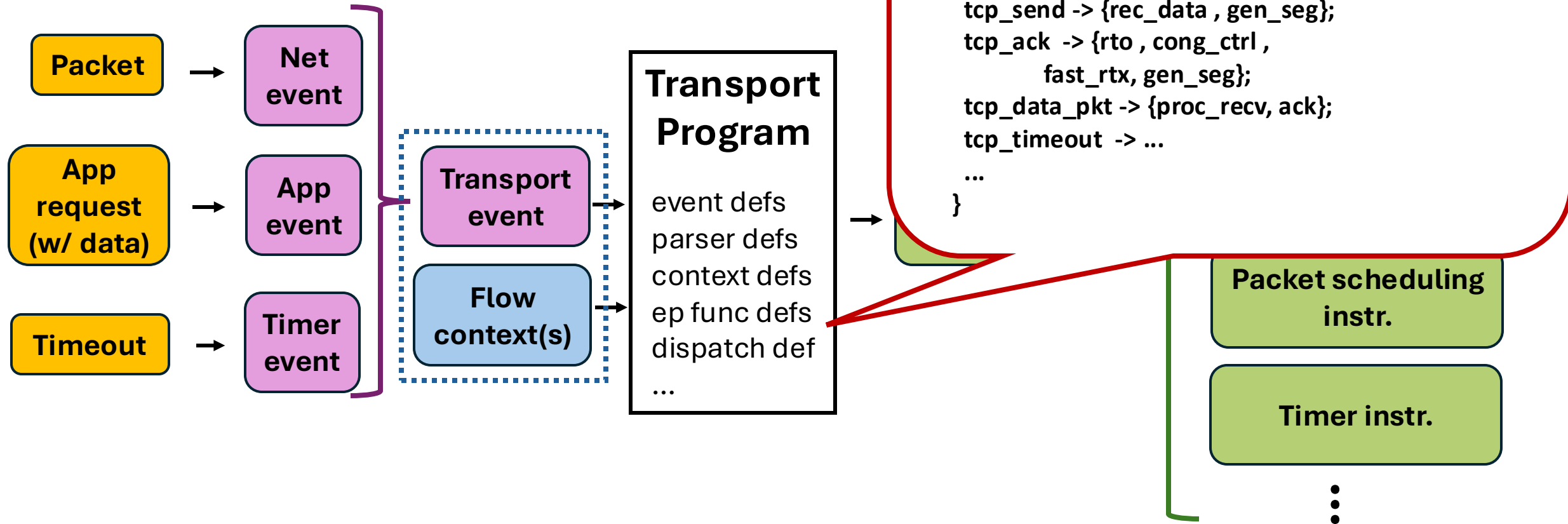
Transport instructions



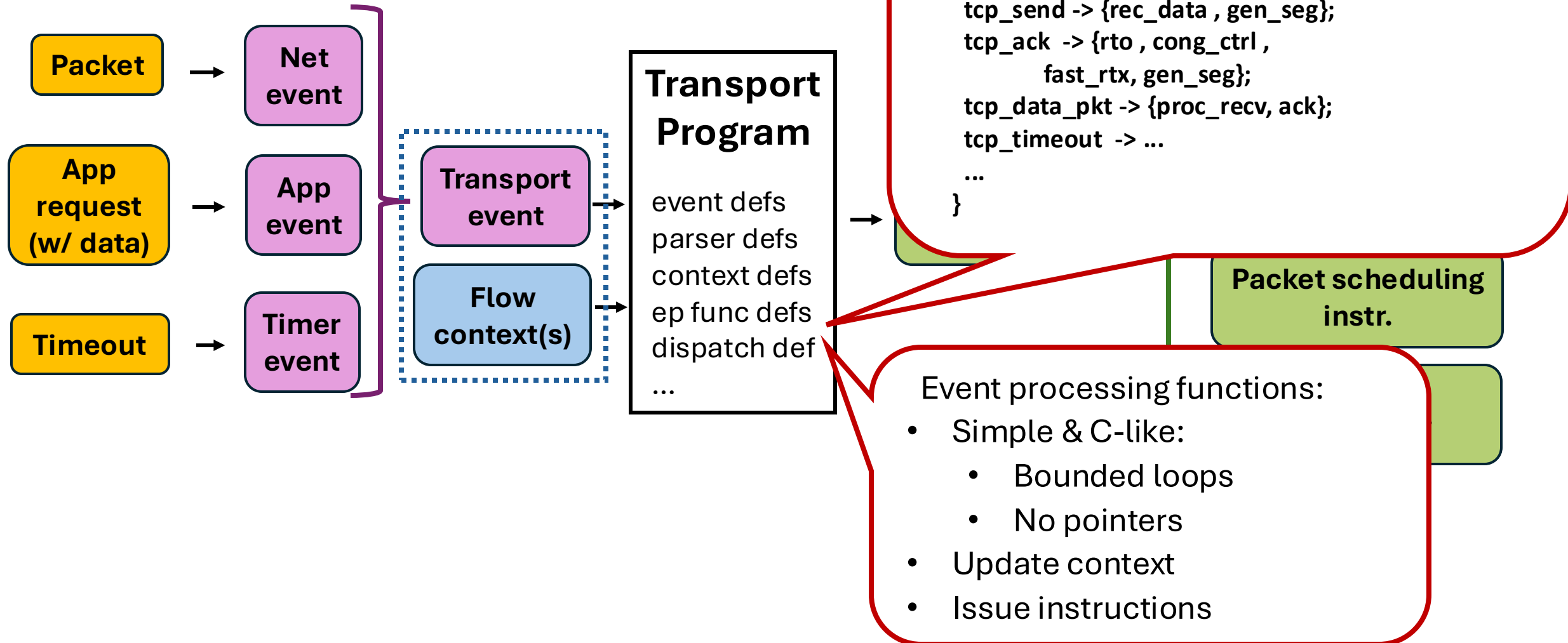
From inputs to outputs



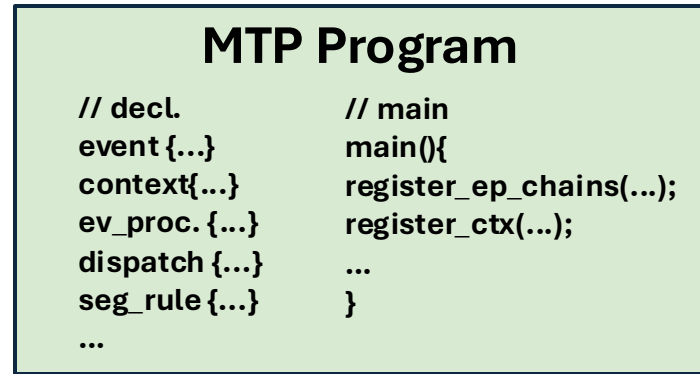
From inputs to outputs



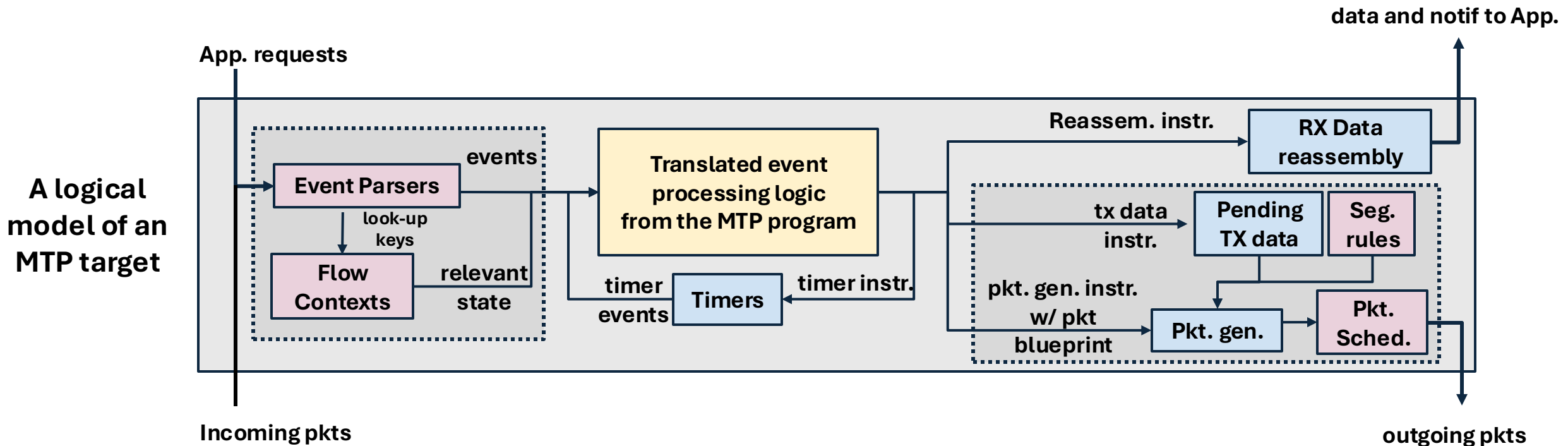
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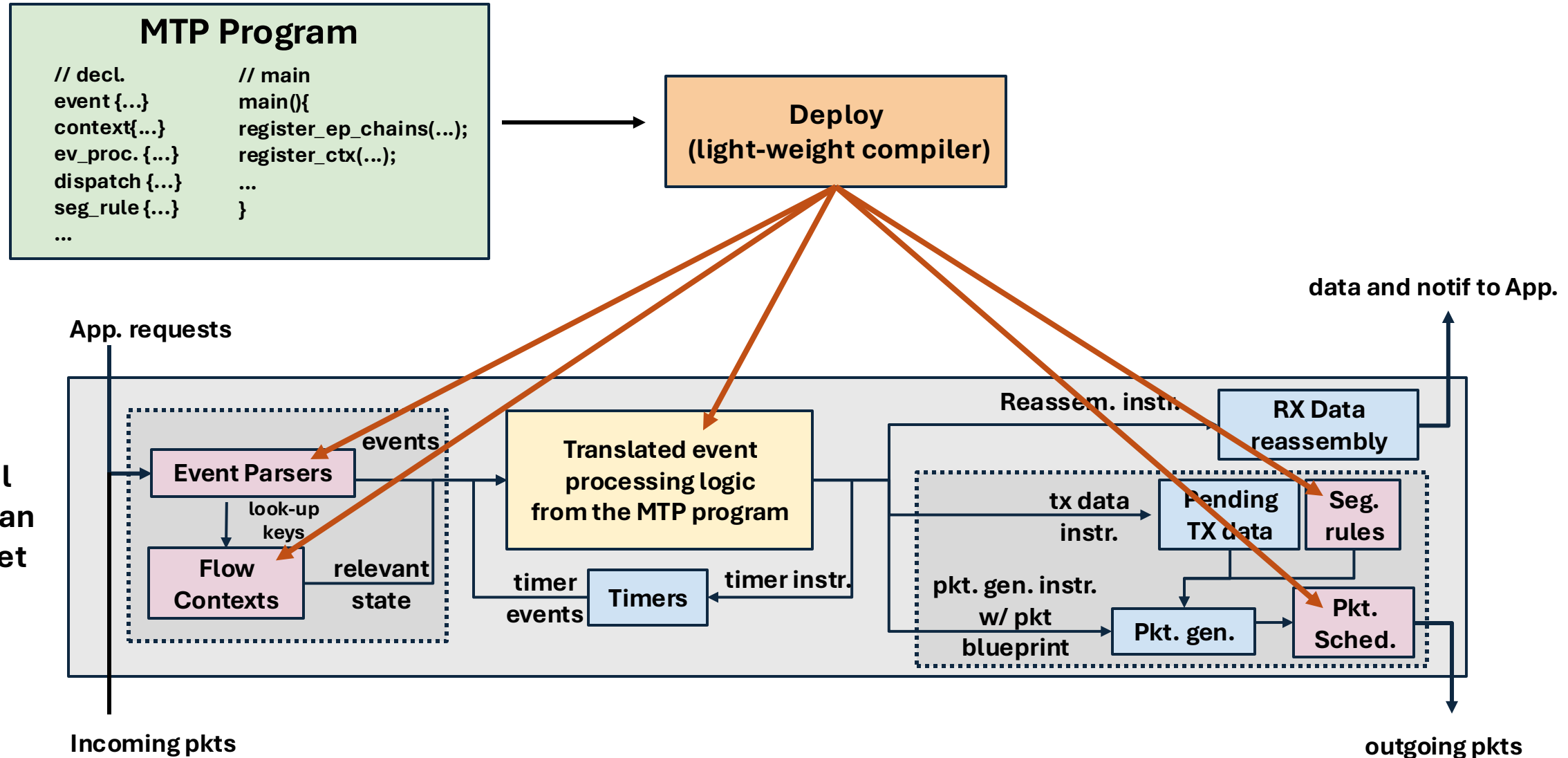
Modular Transport Programming (MTP)



Deploy
(light-weight compiler)



Modular Transport Programming (MTP)



Expressiveness

Ultra Ethernet Transport in MTP?
Short-term future work

- ✓ TCP
 - ✓ QUIC-Lite
- Stream-based
 - Applications append data to byte streams to be sent
 - TCP: one per connection
 - QUIC-Lite: multiple parallel ones per connection
 - Sender-side congestion control
-
- ✓ Homa
 - ✓ NDP
- Message-based
 - Application message size is known (e.g., RPC)
 - Receiver-driven
-
- ✓ RoCEv2
- Message-based
 - Queue pairs as “connections”
 - Designed for hardware

What about performance?

Observation:

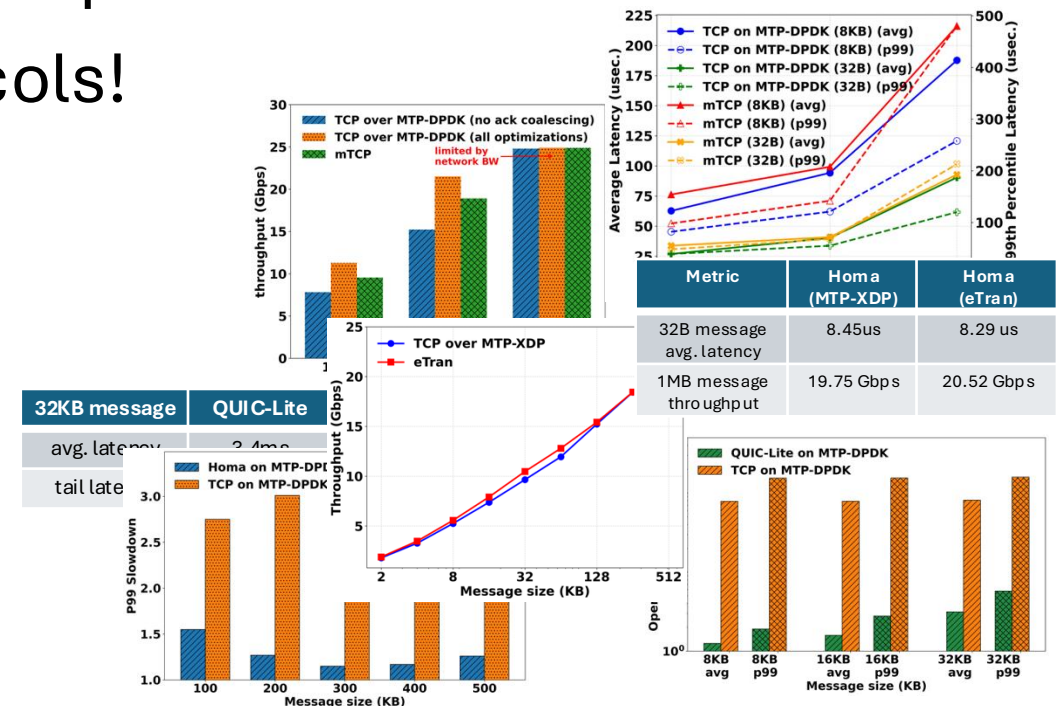
Existing protocol implementations already know how to do transport tasks *efficiently* in a specific execution environment

- e.g., buffer management, packet I/O, per-flow state tracking, ...

We can “refactor” them to expose these tasks via MTP’s high-level unifying interface.

MTP-compliant targets offer comparable performance

- MTP-DPDK and MTP-XDP
 - Refactored an existing TCP implementations over DPDK/XDP
 - mTCP (NSDI'14) and eTran (NSDI'25)
 - To implement MTP's API
- TCP over MTP targets has comparable performance
- It is possible to swap in other protocols!
 - Homa and QUIC-Lite
- See paper for
 - Each target's implementation details
 - Experiment details
 - And plots!



Takeaways from implementing MTP targets

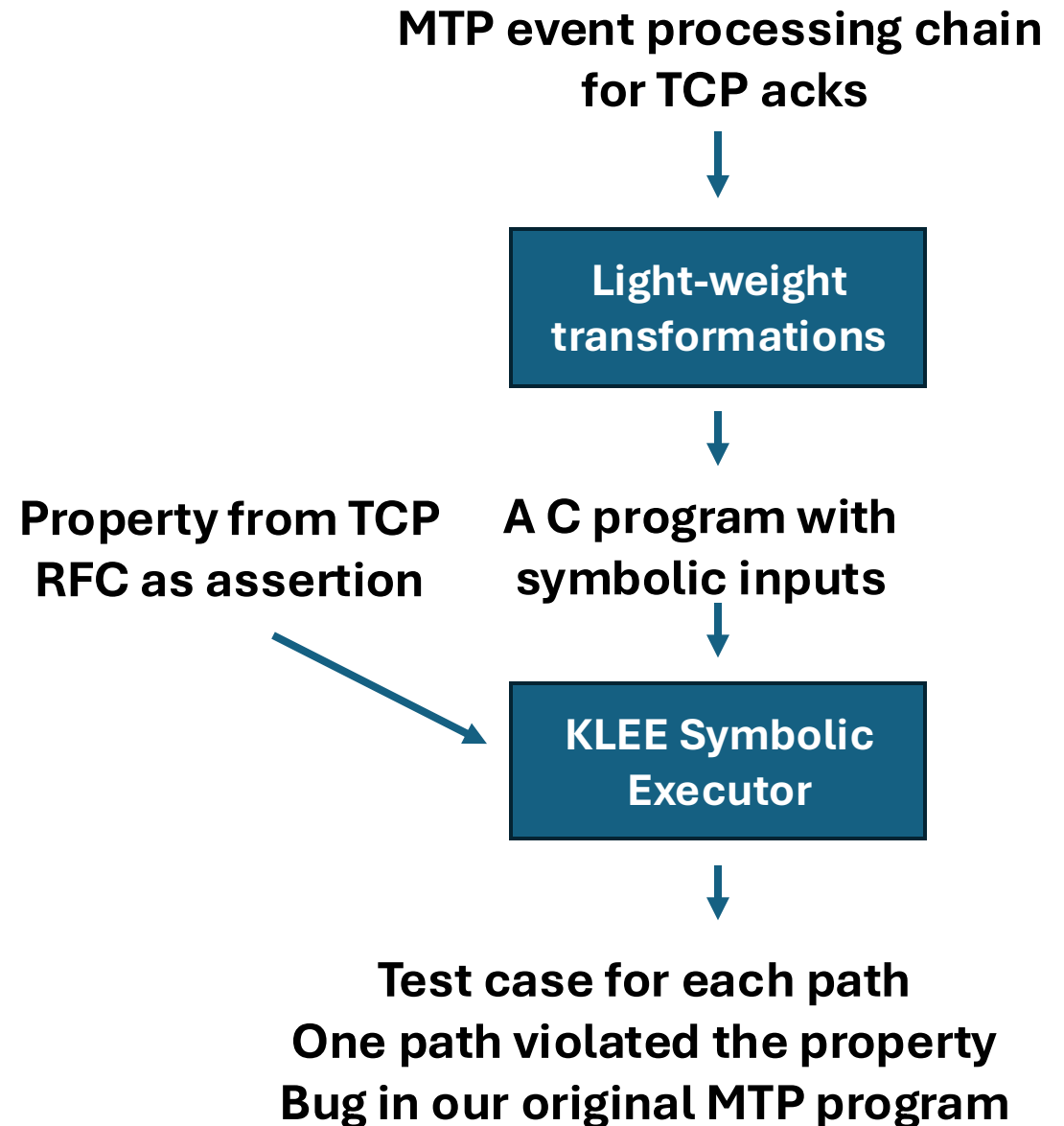
- MTP's API is at the right level of abstraction
 - abstracts away enough details to be target-agnostic
 - implementable with already existing efficient mechanisms
- Different targets' impl. of transport tasks vary in non-trivial ways
 - Confirmed our decision to abstract them as instructions
- The heavy lifting is in implementing the instructions
 - Abstract away most of the complexity
- Translating the event chains can be done with a light-weight compiler

Reduction in development effort

MTP Programs <i>Target-independent</i> <i>Written once</i>	TCP	753 LoC
	Homa	1205 LoC
	QUIC-Lite	920 LoC
MTP-Compliant Targets <i>Protocol-independent</i> <i>Developed once per target</i>	MTP-DPDK	15,593 LoC
	MTP-XDP	14,837 LoC

Automated analysis

- MTP programs are amenable to automated analysis
 - Constrained C-like language
 - no pointers
 - Bounded loops
 - Constrained data structures
 - target-agnostic instructions hiding low-level details



A shout-out to the team!



Pedro Mizuno
UWaterloo



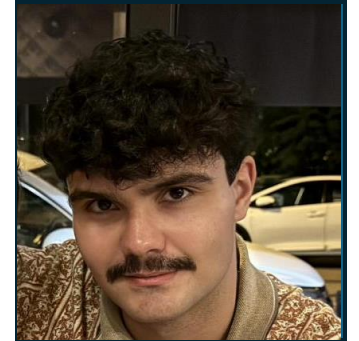
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Danny Akbarzadeh
UWaterloo



Chris Neely
AMD



Mario Baldi
NVIDIA



Nachiket Kapre
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Mina Tahmasbi Arashloo
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Summary and looking forward

- Transport protocols will continue to evolve
- Their execution environments will continue to evolve
 - Software: Kernel, Kernel-bypass, eBPF
 - Hardware accelerators
- This diversity calls for a language abstraction that is *high-level, target-agnostic, and protocol-independent* ...
 - MTP takes a significant step in this direction.
- ... that can unlock a myriad of benefits:
 - Seamlessly swapping in new protocols and add features on a target
 - Automated functional and performance verification
 - Automated testing
 - Write-once run-anywhere
 -