

Automated Test Case Generation from P4 Programs

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ixia
A Keysight Business



KEYSIGHT
TECHNOLOGIES

The Need: Test any arbitrary protocol, conveniently, at line rates

Programmable Data plane (exemplified by P4)

THE NEXT STEP IN SDN

P4 Enables:

- ✓ Protocol Independence
- ✓ Handle existing and future protocols
- ✓ Target Independence
- ✓ Line-rate processing

PROBLEM: How do you test a new protocol with existing line-rate testers?

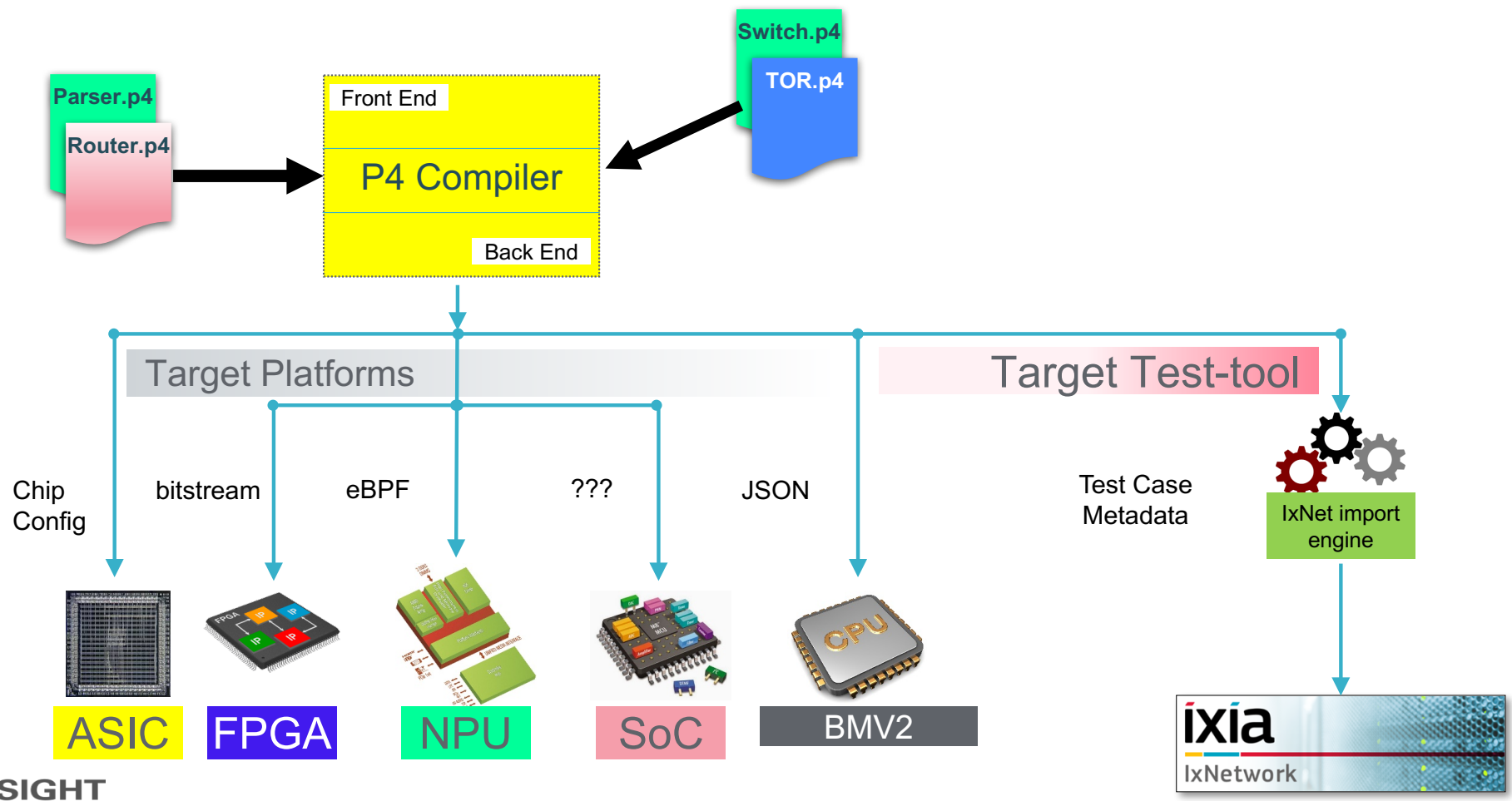
- ☹ new protocols not standardized yet, are experimental or proprietary
- ☹ tools don't generally anticipate unknown protocols, or else handle them inadequately

SOLUTION:

The P4 code which defines the function of a device, can also act as the **specification for the test-tool**.

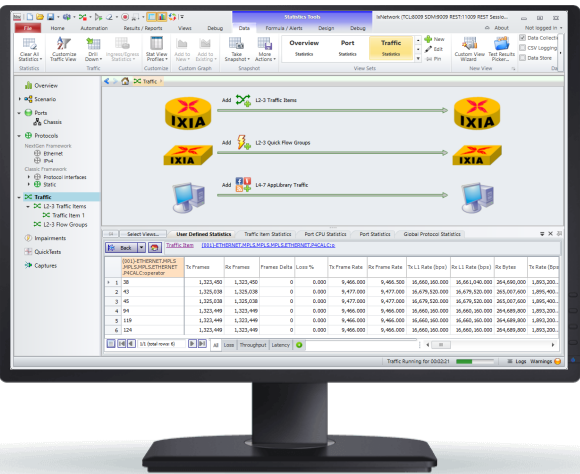
Thus we can achieve a protocol-independent “protocol test tool”. *

WORKFLOW

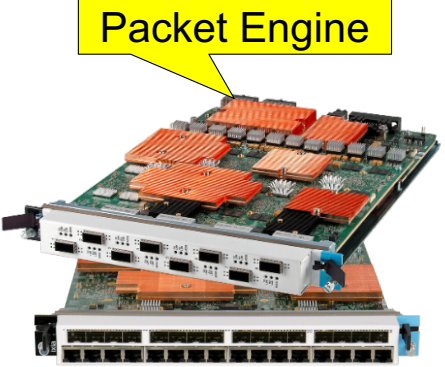
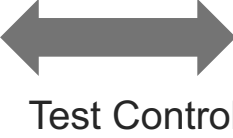


Overview of IxNetwork

IxNetwork + Physical Test Chassis

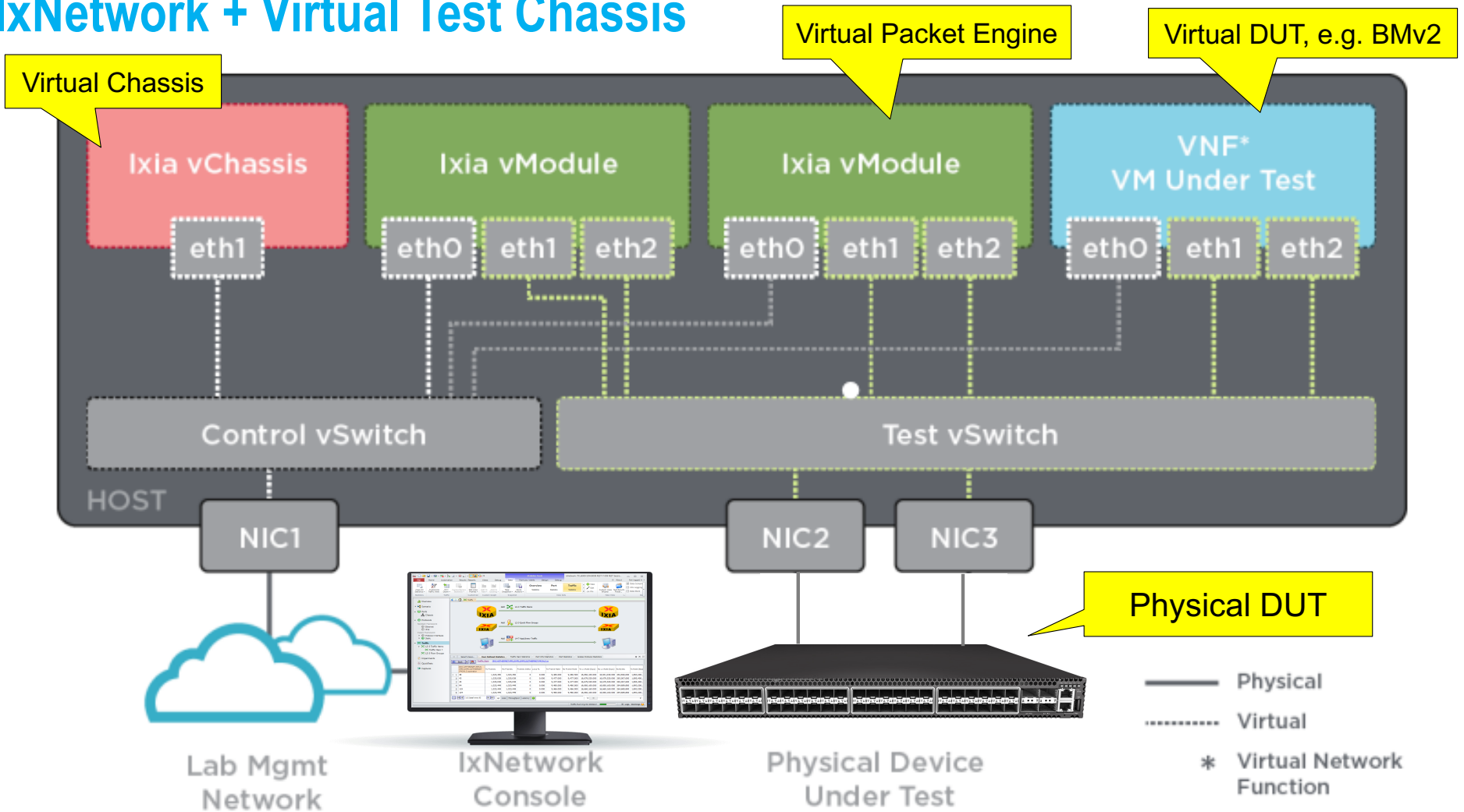


Test Console /IxNetwork Client



Load Modules Up to 400GbE

IxNetwork + Virtual Test Chassis

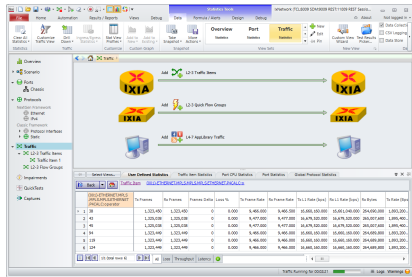
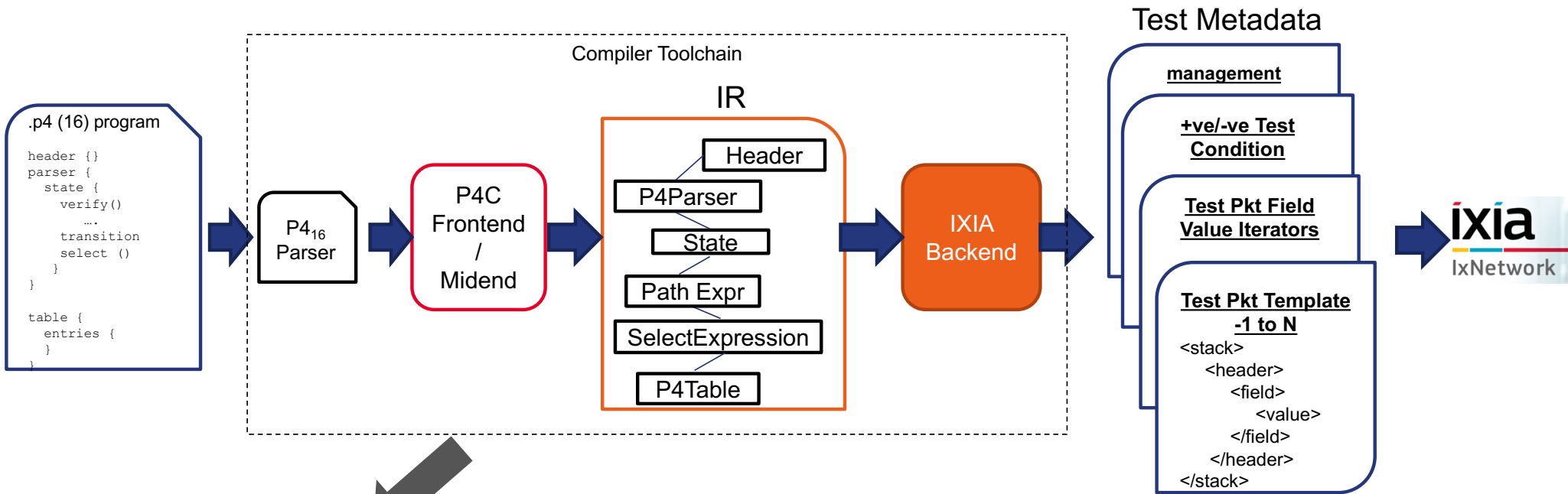


What did we do?

Enhancing IxNetwork to be P4-Aware

- ✓ Create a new backend for the p4c compiler: p4c-ixia. Output is test-case metadata
- ✓ Enhance IxNetwork to embed and launch p4c-ixia and to import the test-case metadata
- ✓ Enhance IxNetwork to translate test-case metadata into test data streams utilizing our packet engines
- ✓ Existing load modules (physical and virtual) are already highly programmable and largely protocol-agnostic. *No modifications were required on the packet engines.*
- ✓ This also allows both our physical and virtual packet testers to support p4 testing.

Architecture



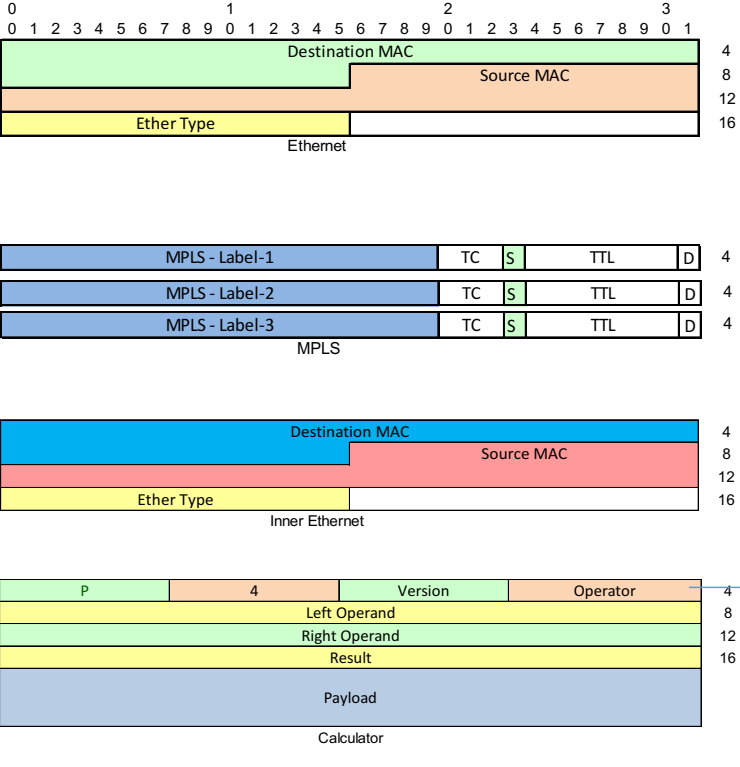
p4c-ixia embedded within IxNetwork

Metadata Packet Structures & Field Values:

- All possible valid sequence of protocol headers (packet templates).
- “Header Stacks” information (eg: multiple MPLS labels)
- Erroneous Packets (eg: hitting the reject rules, exceeding the boundary conditions)
- Packet fields values to test the limit of “Verify” condition
- Packet structure and field values needed to execute “Key Set” for “Select”
- Dealing with constant entries in “P4 Table”.

Calculator Protocol – An arbitrary data-plane as test case

The Data Plane



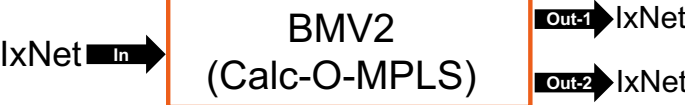
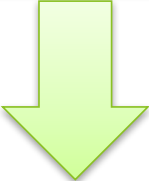
- Header stack
- Validation : pkts with 1~3 labels

OPERATOR is an operation to Perform. It is of 8 bits.

- '+' (0x2b) Result=Left Operand + Right Operand B
- '-' (0x2d) Result=Left Operand - Right Operand B
- '&' (0x26) Result=Left Operand & Operand B.
- '|' (0x7c) Result=Left Operand | Right Operand B
- '^' (0x5e) Result=Left Operand ^ Right Operand B

```

136 * All headers, used in the program needs to be assembled into a single struct.
137 * We only need to declare the type, but there is no need to instantiate it.
138 * because it is done "by the architecture", i.e. outside of P4 functions
139 */
140
141 struct headers
142 {
143     #name("ethernet")
144     ETHERNET ethernet;
145     #name("p4calc")
146     P4CALC p4calc;
147     #name("mpls")
148     MPLS[3] mpls;
149     #name("inner_ethernet")
150     ETHERNET inner_ethernet;
151 }
152
153
154 struct ingress_metadata_t
155 {
156     bit<1> flag;
157 }
158
159 struct metadata
160 {
161     #name("ingress_metadata")
162     ingress_metadata_t ingress_metadata;
163 }
164
165 //----- P A R S E R -----
166 //-----
167
168 parser PacketParser(packet_in packet, out headers hdr, inout metadata meta, inout standard_meta
169 {
170     #name("start") state start
171     {
172         transition parse_ethernet;
173     }
174
175     #name("parse_ethernet") state parse_ethernet
176     {
177         packet.extract(hdr.ethernet);
178         transition select(hdr.ethernet.ethertype)
179         {
180             0x8847 : parse_mpls;
181             default : parse_reject;
182         }
183     }
184 }
185
186 #name("parse_mpls") state parse_mpls
    
```



https://github.com/p4lang/tutorials/blob/master/P4D2_2017_Spring/exercises/calc/solution/calc.p4

Video Demonstration

Results : Templates (Positives & Negatives)

| Stack name | Details |
|--|---|
| "(001)-ETHERNET.MPLS.MPLS.MPLS.MPLS - REJECT " | Rejected as 4 th MPLS stack not supported. |
| "(002)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET.P4CALC" | |
| "(003)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET.P4CALC - REJECT" | Rejected by p4calc version (0x503402 accepted type) |
| "(004)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET - REJECT" | Rejected by inner eitherType (0x1234 accepted type – calc protocol) |
| "(005)-ETHERNET.MPLS.MPLS.ETHERNET.P4CALC" | |
| "(006)-ETHERNET.MPLS.MPLS.ETHERNET.P4CALC - REJECT" | Rejected by p4calc version (0x503402 accepted type) |
| "(007)-ETHERNET.MPLS.MPLS.ETHERNET - REJECT" | Rejected by inner eitherType (0x1234 accepted type – calc protocol) |
| "(008)-ETHERNET.MPLS.ETHERNET.P4CALC" | |
| "(009)-ETHERNET.MPLS.ETHERNET.P4CALC - REJECT" | Rejected by p4calc version (0x503402 accepted type) |
| "(010)-ETHERNET.MPLS.ETHERNET - REJECT" | Rejected by inner eitherType (0x1234 accepted type – calc protocol) |
| "(011)-ETHERNET - REJECT" | Rejected by outer eitherType (0x8847 accepted type) |

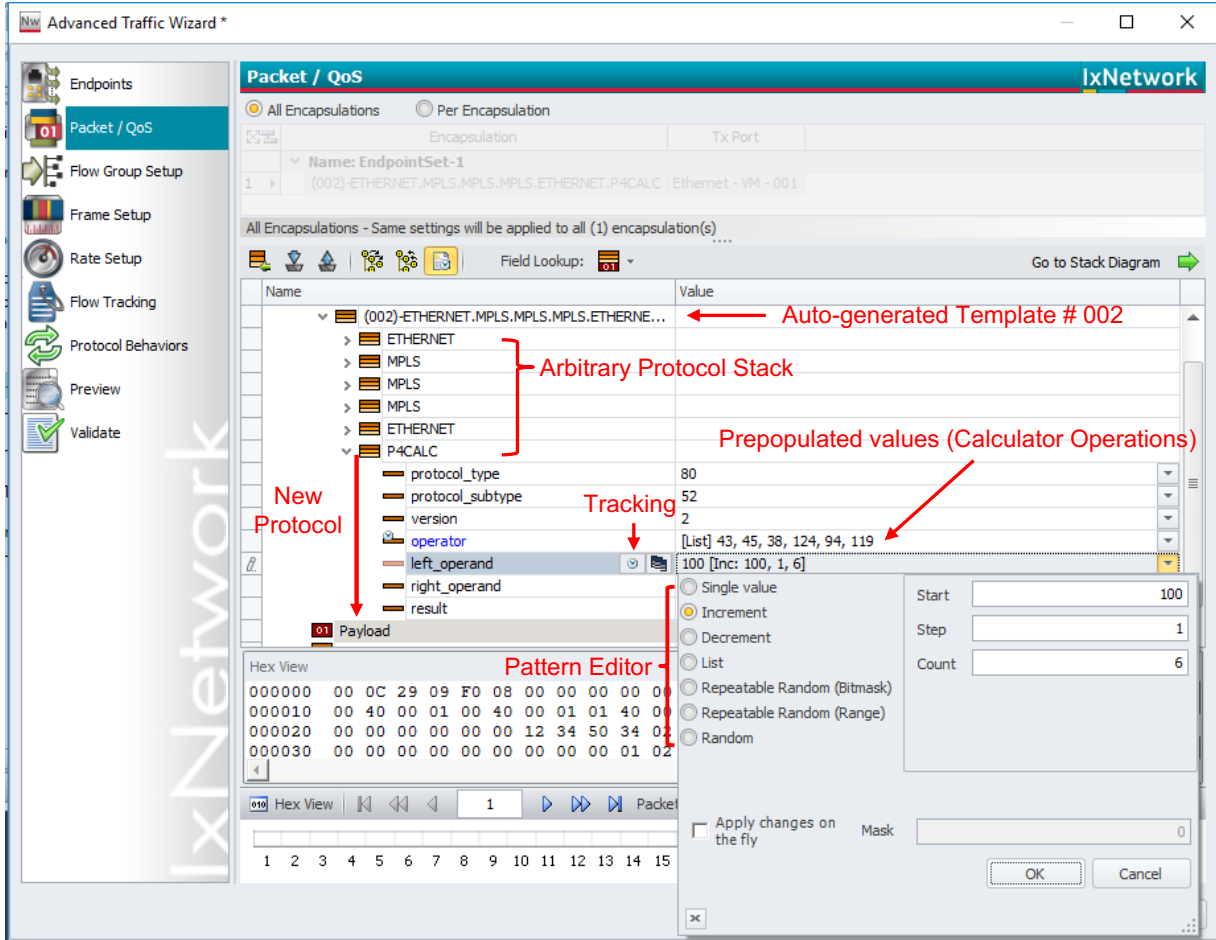
Select Protocol ✕

Search P4 File

| P4 File | Protocol |
|-----------------------|--|
| eth-mpls-3-eth-p4calc | (001)-ETHERNET.MPLS.MPLS.MPLS.MPLS - REJECT |
| eth-mpls-3-eth-p4calc | (002)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET.P4CALC |
| eth-mpls-3-eth-p4calc | (003)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET.P4CALC - REJECT |
| eth-mpls-3-eth-p4calc | (004)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET - REJECT |
| eth-mpls-3-eth-p4calc | (005)-ETHERNET.MPLS.MPLS.ETHERNET.P4CALC |
| eth-mpls-3-eth-p4calc | (006)-ETHERNET.MPLS.MPLS.ETHERNET.P4CALC - REJECT |
| eth-mpls-3-eth-p4calc | (007)-ETHERNET.MPLS.MPLS.ETHERNET - REJECT |
| eth-mpls-3-eth-p4calc | (008)-ETHERNET.MPLS.ETHERNET.P4CALC |
| eth-mpls-3-eth-p4calc | (009)-ETHERNET.MPLS.ETHERNET.P4CALC - REJECT |
| eth-mpls-3-eth-p4calc | (010)-ETHERNET.MPLS.ETHERNET - REJECT |
| eth-mpls-3-eth-p4calc | (011)-ETHERNET - REJECT |

Results: What does the Ixia Traffic Engine see & do ?

- ✓ User friendly mechanism to vary any protocol fields.
- ✓ Use Ixia's powerful pattern editor to vary the fields. Underneath Ixia UDF(s) are used to support variation.
- ✓ Flexibility of any fields to track (including the new protocol) at Line rate.
- ✓ Ingress and Egress tracking support.
- ✓ Track on meta data (Frame size, Flow Group etc.).
- ✓ Facility to utilize the latency bin(s)
- ✓ Flow Grouping - lowest level of control on Frame rate / size / start & stop



In this way we achieve a protocol independent “protocol test tool”

Invalid field values (for negative test case 003)

- ✓ Fields are pre-populated with invalid values.
- ✓ For Calculator Protocol, type should be ASCII 'P' (decimal 80) and Subtype should be ASCII '4' (decimal 52).
- ✓ Fields are pre-populated with all 8-bit values *except* the valid ones.
- ✓ Pattern Editor customization
- ✓ Similarly, for version field, field is pre-populated with all values other than 2, to verify target behavior for reject scenario.
- ✓ And so forth...

Advanced Traffic Wizard * IxNetwork

Packet / QoS

All Encapsulations Per Encapsulation

Name: EndpointSet-1

(003)-ETHERNET.MPLS.MPLS.MPLS.ETHERNE... Ethernet - VM - 001

All Encapsulations - Same settings will be applied to all (1) encapsulation(s)

Field Lookup: 01

| Name | Value |
|--|---|
| (003)-ETHERNET.MPLS.MPLS.MPLS.ETHERNE... | |
| ETHERNET | |
| MPLS | |
| MPLS | |
| MPLS | |
| ETHERNET | |
| P4CALC | |
| protocol_type | [List] 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19,... |
| protocol_subtype | [List] 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19,... |
| version | [List] 0, 1, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 2 |
| operator | |
| left_operand | |
| right_operand | |
| result | |

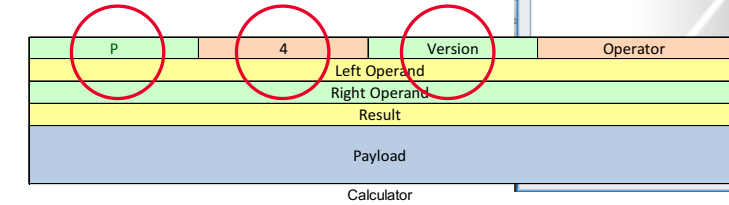
Hex View

```

000000 00 00 00 00 00 00 00 00 00 00 00 00
000010 00 40 00 01 00 40 00 01 01 40 00
000020 00 00 00 00 00 00 12 34 00 00 00
000030 00 00 00 00 00 00 00 00 00 01 02
  
```

Calculator

Prepopulated: All values except 2 (Version)



Results : Tracking based on arbitrary fields

- ✓ Packet generation at Line rate.
- ✓ Drill down statistics based on the tracking fields (including arbitrary fields in arbitrary protocol)
- ✓ Simulates thousands of packets in specific order and verify correct order and latency

The screenshot shows the IxNetwork Traffic Statistics interface. At the top, there are tabs for Overview, Port, and Traffic. The Traffic tab is active, showing a traffic flow diagram with three IXIA routers and a PC icon. Below the diagram, there are three traffic items: L2-3 Traffic Items, L2-3 Quick Flow Groups, and L4-7 AppLibrary Traffic. At the bottom, there is a table of statistics for a specific traffic item.

**Tracking & Drill-down on arbitrary field(s)
Granular statistics**

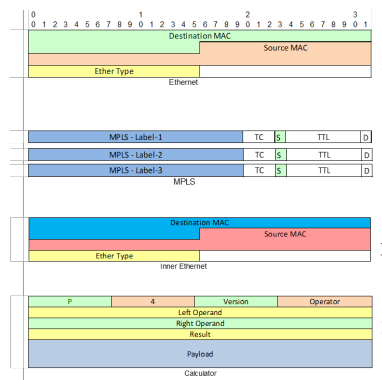
| | (001)-ETHERNET.MPLS.MPLS.MPLS.ETHERNET.P4CALC:operator | Tx Frames | Rx Frames | Frames Delta | Loss % | Tx Frame Rate | Rx Frame Rate | Tx L1 Rate (bps) | Rx L1 Rate (bps) | Rx Bytes | Tx Rate (Bps) |
|---|--|-----------|-----------|--------------|--------|---------------|---------------|------------------|------------------|-------------|---------------|
| 1 | 38 | 1,323,450 | 1,323,450 | 0 | 0.000 | 9,466.000 | 9,466.500 | 16,660,160.000 | 16,661,040.000 | 264,690,000 | 1,893,200.. |
| 2 | 43 | 1,325,038 | 1,325,038 | 0 | 0.000 | 9,477.000 | 9,477.000 | 16,679,520.000 | 16,679,520.000 | 265,007,600 | 1,895,400.. |
| 3 | 45 | 1,325,038 | 1,325,038 | 0 | 0.000 | 9,477.000 | 9,477.000 | 16,679,520.000 | 16,679,520.000 | 265,007,600 | 1,895,400.. |
| 4 | 94 | 1,323,449 | 1,323,449 | 0 | 0.000 | 9,466.000 | 9,466.000 | 16,660,160.000 | 16,660,160.000 | 264,689,800 | 1,893,200.. |
| 5 | 119 | 1,323,449 | 1,323,449 | 0 | 0.000 | 9,466.000 | 9,466.000 | 16,660,160.000 | 16,660,160.000 | 264,689,800 | 1,893,200.. |
| 6 | 124 | 1,323,449 | 1,323,449 | 0 | 0.000 | 9,466.000 | 9,466.000 | 16,660,160.000 | 16,660,160.000 | 264,689,800 | 1,893,200.. |

What's Next ?

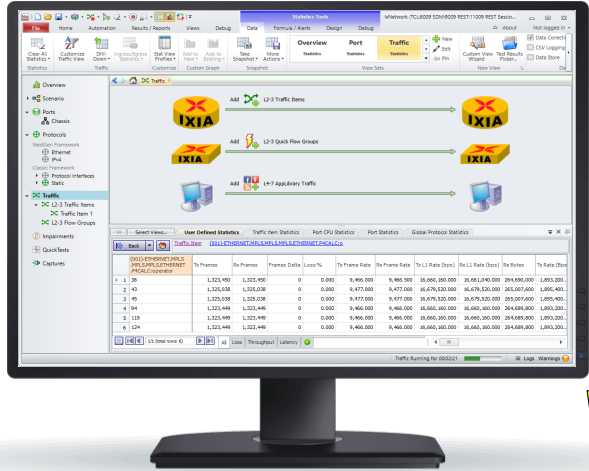
- ✓ Focus on testing, stability and react to community feedback.
- ✓ Automatic packet decoder

```
⊕ Frame 23: 200 bytes on wire (1600 bits), 200 bytes captured (1600 bits) on interface 0
⊕ Ethernet II, Src: aa:00:00:00:00:01 (aa:00:00:00:00:01), Dst: 00:0c:29:09:f0:08 (00:0c:29:09:f0:08)
⊕ MultiProtocol Label Switching Header, Label: 10016, Exp: 0, S: 0, TTL: 64
⊕ MultiProtocol Label Switching Header, Label: 20016, Exp: 0, S: 0, TTL: 64
⊕ MultiProtocol Label Switching Header, Label: 30016, Exp: 0, S: 1, TTL: 64
⊕ Ethernet II, Src: aa:00:00:00:00:01 (aa:00:00:00:00:01), Dst: 00:0c:29:09:f0:08 (00:0c:29:09:f0:08)
⊖ Calculator Protocol
  Version: 2
  Operator: PLUS (0x2b)
  Left operand: 100
  Right operand: 10
  Result: 110
```

- ✓ Stateful Fuzzing of arbitrary protocol



Future possibility - Control Plane Integration

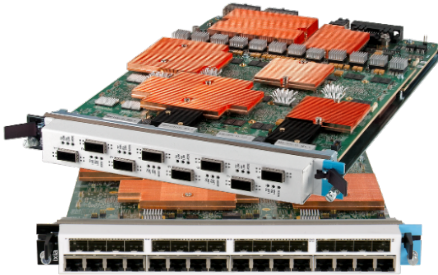


Test Console /IxNetwork Client

Test Control



Ixia Chassis



Load Modules Up to 400GbE

Control Plane
Stratum, etc.

Traffic



Device Under Test (DUT)

Thank You

Questions?